

Raghav Kalra

Electronics Engineer

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SUMMARY

I am currently pursuing a B.Tech in Electronics Engineering from RGIPT (an Institution of National Importance established along the lines of IITs, Government of India). As a B.Tech student in Electronics Engineering at RGIPT, I am dedicated to advancing my knowledge and skills in electronics and technology. My primary interest lies in VLSI technology and VLSI design with HDL

SKILLS

Languages: C, C++, Python, Verilog

Boards: Arduino, Spartan-7 FPGA

Soft Skills: Leadership, Writing, Public Speaking, Time Management

EXPERIENCE

Research Intern

Indian Institute of Technology Mandi

May 2024 – July 2024

Mandi, Himachal Pradesh

- Designed a floating-point unit (FPU) based on IEEE single precision.
- Developed dual-port memory for data and instruction storage.
- Implemented UART with 19200 baud rate for data transmission.
- Designed program counter and control register for FPU control.
- Performed successful testing of each module using simulations.

PROJECTS

ALU using Verilog HDL

- Developed an ALU to perform arithmetic operations like addition, subtraction, multiplication, division, and square root using Verilog HDL.

FSM for RGB colors ball detection

- Developed a Mealy-based FSM in Verilog to detect sequences of colored balls.
- Tested and verified design using a simulation test bench.

Banking Interface using C Programming

- Developed a system with functions for account creation, deposits, withdrawals, and balance checks.

EDUCATION

2022 – 2026	Rajiv Gandhi Institute of Petroleum Technology, Amethi, Uttar Pradesh Bachelor's of Technology - Electronics Engineering (Till 4th sem CGPA: 8.3/10.0)
2022	Chand Ram Public School, Delhi Intermediate Certificate (Class 12th: 84.8%)
2020	St. Mary's School, Hisar, Haryana Matriculation Certificate (Class 10th: 89.8%)

RELEVANT COURSEWORK

Engineering Physics, Engineering Mathematics, Digital Electronics, Solid State Devices, Network Analysis, Signal and Systems, Analog Electronics, Material Science, Control Systems

POSITION OF RESPONSIBILITY

Event Management Head, IEEE RGIPT Student Branch

Nov 2023 - Present

- Organized seminars and webinars, developed leadership and time management skills.